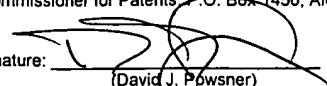




I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being deposited with the U.S. Postal Service on the date shown below with sufficient postage as First Class Mail, in an envelope addressed to: Attention: Certificate of Correction Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.
Dated: 10/11/06 Signature: 
(David J. Powsner)

09/758798 C O P O

Docket No.: 102323-0062
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Letters Patent of:
Robert C. Frisch et al.
Patent No.: 7,106,742
Issued: September 12, 2006

For: METHOD AND SYSTEM FOR LINK FABRIC
ERROR DETECTION AND MESSAGE FLOW
CONTROL

**REQUEST FOR CERTIFICATE OF CORRECTION
PURSUANT TO 37 CFR 1.322**

Attention: Certificate of Correction Branch
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Certificate
OCT 19 2006
of Correction

Dear Sir:

Upon reviewing the above-identified patent, Patentee noted a typographical error which should be corrected.

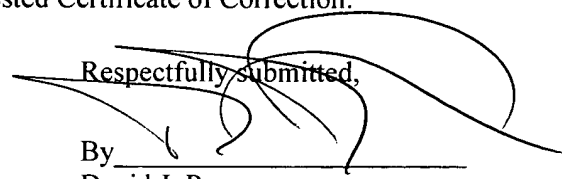
In the Claims:

In Claim 5, line 10, following "more", please delete "messages".

The error was not in the application as filed by applicant; accordingly no fee is required.

Transmitted herewith is a proposed Certificate of Correction effecting such amendment. Patentee respectfully solicits the granting of the requested Certificate of Correction.

Dated: 10/11/06


Respectfully submitted,

By
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OCT 19 2006

**UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION**

Page 1 of 1

PATENT NO. : 7,106,742
APPLICATION NO. : 09/758,798
ISSUE DATE : September 12, 2006
INVENTOR(S) : Robert C. Frisch, Daniel L. Bourier, and Bryan D. Marietta

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

**** In Claim 5, line 10, following "more", please delete "messages".**

Certificate of Correction

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Dated: 10/11/06

Signature: [Signature]

(David J. Powsner)

1569503.1

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OCT 19 2006

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What is claimed is:

1. A digital data system comprising
a link that carries message packets,
a first node sending a plurality of message packets to a
second node on the link as a sequence of message packets, with each message packet being sent from the first node with a sequence identifier, and each message packet comprising an initial portion comprising an error code,
the second node (i) inspecting the error code for each packet received on the link to detect an error condition, and (ii) returning a control symbol along with the sequence identifier of the received packet to the first node based on the result of that inspection,
the second node returning the control symbol to the first node before the entire message packet has arrived at the second node, and
the first node responding to the control symbol to control the further transmission of message packets to the second node over the link
wherein the second node returns a packet-not-accepted control symbol to the first node indicating receipt on the link of a message packet that is out of sequence and wherein the first node responds to the packet-not-accepted control symbol by re-sending a portion of the sequence of message packets.
2. A digital data system according to claim 1, wherein the first node queries the second node for an identifier of a message packet in the sequence with which to begin re-sending.
3. A digital data system comprising:
first and second nodes connected by a first link,
the first node sending a plurality of message packets to the second node over the first link, each message packet including a header portion and a further portion, the header portion including an error code,
the second node checking the error code and sending a valid message packet to a further node over a further link,
at least a part of the header portion being a changeable part that may change as the message packet passes from the first link to the further link, and at least a part of the message packet being an invariant part that does not change,
whereby the error code need not to be recalculated when the message packet passes to the further link,
whereby corruption of the header portion of the packet is detected before the second node has received the entire packet, and without reference to the further portion of the packet,
and wherein the second node returning a control symbol to the first node before the entire message packet has arrived at the second node.

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4. A digital data system according to claim 3, wherein the changeable part includes a sequence identifier, and the second node compares the changeable part of a message packet with an expected sequence identifier to detect an error.

5. A digital system comprising:

first and second nodes connected by a first link,

the first node sending a data from a buffer as a transmission sequence of one or more ~~messages~~ message packets to the second node over the first link, each message packet including a sequence identifier in an initial portion of the message packet,

the second node checking the initial portion to identify a faulty message reception,

and communicating said sequence identifier to the first node with a control symbol indicating whether reception was proper such that the first node may respond to the control symbol by clearing the buffer or retransmitting at least a portion of the transmission sequence, whereby corruption of the initial portion is detected without reference to a subsequent portion of the packet, and wherein the second node returning the control symbol to the first node before the entire message packet has arrived at the second node.

6. A digital data system comprising:

first and second nodes connected by a first link,

the first node sending a plurality of message packets to the second node over the first link with each message packet being sent from the first node with a sequence identifier, and each message packet comprising an initial portion comprising an error code,

the second node sending the message packets to a further node over a further link,

the second node (i) inspecting the error code for each message packet received on the link to detect an error condition, and (ii) returning a control symbol to the first node for each packet received therefrom the first link along with the sequence identifier of the received message packet based on the result of that inspection,

the second node returning the control symbol to the first node before the entire message packet has arrived at the second node,

the first node responding to the control symbol to control the further transmission of the message packets to the second node over the first link,

such that transmission of data packets from the first node to the further node proceeds efficiently.

* * * * *

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